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In re Patent Application of:
ROCHE ET AL.
Serial No. 10/039,765
Filing Date: NOVEMBER 7, 2001

REMARKS

Applicants would like to thank the Examiner for the thorough examination of the present application, and for the courtesies extended in the telephone interview on August 31, 2006.

In the Official Action, the Examiner maintained his position that the I²C Bus Specification discloses all of the elements of the claimed invention. The Examiner further states that some of the arguments made in the Amendment After Final are not recited in the claims, and consequently, are not given any patentable weight.

In the telephone interview, the Examiner stated what arguments could be better highlighted in the claims. The Examiner made reference to FIG. 2A in which "tying the clock line CK to 0" in Block M12 is performed "after bit b is applied to the data line DT" in Block M11. As a result, the independent claims have been amended to better highlight that the clock line is tied to the second logic value, via the two devices, after data is applied to the data line. As noted by the Examiner, since the claim recitation of "after" replaces "when," this removes any question of the claims being indefinite. The Examiner further stated that this would overcome the current rejection.

In view of the claim amendments and the foregoing remarks, it is submitted that all of the claims are patentable. Accordingly, a Notice of Allowance is respectfully requested in due course. Should any minor informalities need to be addressed, the Examiner is encouraged to contact the undersigned attorney at the telephone number listed below.

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Respectfully submitted,

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CERTIFICATE OF FACSIMILE TRANSMISSION

I HEREBY CERTIFY that the foregoing correspondence
has been forwarded via facsimile number 571-273-8300 to the
Commissioner for Patents on this 15 day of September, 2006.

Karen Dravosky